Applicant Information

Applicant Authority Type:: Inventor

Applicant One Given Name:: Chia-Lin

Family Name:: Chen

Postal Address Line One:: 5F, NO. 111, Minshiang St

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Applicant Authority Type:: Inventor

Applicant Two Given Name:: Tze-Liang

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Applicant Authority Type:: Inventor

Applicant Three Given Name:: Shih-Chang

Family Name:: Chen

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Correspondence Information

Correspondence Customer Number::000027683

Application Information

Title Line One:: SEMICONDUCTOR WAFER MANUFACTURING

Title Line Two:: METHODS EMPLOYING CLEANING DELAY

Title Line Three:: PERIOD

Total Drawing Sheets:: 3

Formal Drawings?:: Yes

Application Type:: Utility

Docket Number:: 24061.42 (TSMC2002-1015

Representative Information

Registration Number:: 42,044

Assignee Information

Assignee Name:: Taiwan Semiconductor Manufacturing Co., Ltd.

Street of mailing address:: No. 8, Li-Hsin Rd. 6, Science-Based Industrial Park

City:: Hsin-Chu

State or Province:: Taiwan, R.O.C.

Country of mailing address:: Taiwan, R.O.C.

Postal or Zip Code:: 300-77